

09/988984

PTO/SB/17 (07-06)

Approved for use through 01/31/2007. OMB 0651-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Effective on 12/08/2004. Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818). <b>FEE TRANSMITTAL</b> <b>For FY 2006</b>		<b>Complete if Known</b>	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27		Application Number	Patent#: 6,815,818
		Filing Date	Issued: November 9, 2004
		First Named Inventor	John T. Moore
		Examiner Name	T. V. Pham
		Art Unit	2823
<b>TOTAL AMOUNT OF PAYMENT</b>		Attorney Docket No.	M4065.0608/P608
(\$) 100.00			

<b>METHOD OF PAYMENT</b> (check all that apply)	
<input type="checkbox"/> Check	<input checked="" type="checkbox"/> Credit Card
<input type="checkbox"/> Money Order	<input type="checkbox"/> None
<input type="checkbox"/> Other (please identify): _____	
<input checked="" type="checkbox"/> Deposit Account	Deposit Account Number: 04-1073
Deposit Account Name: Dickstein Shapiro LLP	
For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)	
<input type="checkbox"/> Charge fee(s) indicated below	<input type="checkbox"/> Charge fee(s) indicated below, except for the filing fee
<input type="checkbox"/> Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17	<input checked="" type="checkbox"/> Credit any overpayments

<b>FEE CALCULATION</b>							
<b>1. BASIC FILING, SEARCH, AND EXAMINATION FEES</b>							
	<b>FILING FEES</b>		<b>SEARCH FEES</b>		<b>EXAMINATION FEES</b>		
		<b>Small Entity</b>		<b>Small Entity</b>		<b>Small Entity</b>	
<b>Application Type</b>	<b>Fee (\$)</b>	<b>Fee (\$)</b>	<b>Fee (\$)</b>	<b>Fee (\$)</b>	<b>Fee (\$)</b>	<b>Fee (\$)</b>	<b>Fees Paid (\$)</b>
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	
<b>2. EXCESS CLAIM FEES</b>							
						<b>Small Entity</b>	
<b>Fee Description</b>						<b>Fee (\$)</b>	<b>Fee (\$)</b>
Each claim over 20 (including Reissues)						50	25
Each independent claim over 3 (including Reissues)						200	100
Multiple dependent claims						360	180
<b>Total Claims</b>		<b>Extra Claims</b>		<b>Fee (\$)</b>		<b>Fee Paid (\$)</b>	
- =		x		=			
HP = highest number of total claims paid for, if greater than 20.							
<b>Indep. Claims</b>		<b>Extra Claims</b>		<b>Fee (\$)</b>		<b>Fee Paid (\$)</b>	
- =		x		=			
HP = highest number of independent claims paid for, if greater than 3.							
<b>3. APPLICATION SIZE FEE</b>							
If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).							
<b>Total Sheets</b>		<b>Extra Sheets</b>		<b>Number of each additional 50 or fraction thereof</b>		<b>Fee (\$)</b>	
- 100 =		/50		(round up to a whole number) x		=	
						<b>Fee Paid (\$)</b>	
<b>4. OTHER FEE(S)</b>							
Non-English Specification, \$130 fee (no small entity discount)						<b>Fees Paid (\$)</b>	
Other (e.g., late filing surcharge): 1811 Certificate of correction						100.00	

<b>SUBMITTED BY</b>			
Signature		Registration No. (Attorney/Agent)	28,371
Name (Print/Type)	Thomas J. D'Amico	Telephone	(202) 420-2232
		Date	January 4, 2007

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**Certificate**  
**JAN 10 2007**  
**of Correction**



Docket No.: M4065.0608/P608  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:  
John T. Moore et al.

Patent No.: 6,815,818

Issued: November 9, 2004

For: ELECTRODE STRUCTURE FOR USE IN AN  
INTEGRATED CIRCUIT

**REQUEST FOR CERTIFICATE OF CORRECTION  
PURSUANT TO 37 CFR 1.323**

Attention: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentees noted errors which were not included in our Request for Certificate of Correction, filed May 11, 2006, which Certificate was subsequently issued by the PTO on September 26, 2006.

The errors we now wish to correct are found in claims 17, 18, 19 and 20.

Column 12, line 1, "electrode structure" should read --semiconductor die--;

Column 12, line 6, "electrode structure" should read --semiconductor die--;

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01 FC:1811

100.00 OP

Column 12, line 8, "electrode structure" should read --semiconductor die--;

and

Column 12, line 11, "electrode structure" should read --semiconductor die--.

The errors were found in the application as filed by Applicants. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

A semiconductor die is described in claim 16, so as claims 17, 18, 19 and 20 depend from claim 16, they are being corrected to include that term.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentees respectfully solicit the granting of the requested Certificate of Correction.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0608/P608.

Dated: January <sup>5</sup>/<sub>4</sub>, 2007

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

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PATENT NO. : 6,815,818  
APPLICATION NO. : 09/988,984  
ISSUE DATE : November 9, 2004  
INVENTORS : John T. Moore et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claims 17, 18, 19 and 20, the following errors are corrected:

Column 12, line 1, "electrode structure" should read --semiconductor die--;

Column 12, line 6, "electrode structure" should read --semiconductor die--;

Column 12, line 8, "electrode structure" should read --semiconductor die--; and

Column 12, line 11, "electrode structure" should read --semiconductor die--.

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